



Our Reference: DFS-138-A

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:  
Serial Number:  
Filing Date:  
Examiner/Art Group Unit:  
Title:

Martin Gehrke & Torsten Pechstein  
10/001,668  
October 23, 2001  
Unknown/Unknown  
A DESIGN FOR CONSTRUCTING AN INPUT  
CIRCUIT TO RECEIVE AND PROCESS AN  
ELECTRICAL SIGNAL

SUBMITTAL OF PRIORITY DOCUMENT

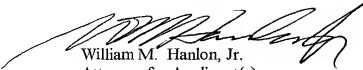
Assistant Commissioner of Patents  
Washington, D.C. 20231

Sir:

The attorney for Applicant(s) respectfully submits, for filing with the  
subject patent application, the German priority document 100 52 532.6.

Respectfully submitted,

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Dated: May 7, 2002  
WMH/jao

## ARTIFACT SHEET

Indicate quantity of item(s) received by the USPTO but not scanned.

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Priority Document No. 100 52 532-6

Country

Deutschland

Doc Code: FRPR

☐

CD(s) containing computer program listing

Doc Code: Computer

☐

Stapled Set(s) of Extra Color Drawings

Doc Code: Artifact

☐

CD(s) containing pages of specification

and/or sequence listing ☐

Doc Code: Artifact

☐

CD(s) with content unspecified

Doc Code: Artifact

☐

Microfilm(s)

Doc Code: Artifact

☐

Video tape(s)

Doc Code: Artifact

☐

Other mass storage media, description: \_\_\_\_\_

Doc Code: Artifact

☐

Model(s)

Doc Code: Artifact

☐

Other, description: \_\_\_\_\_

Doc Code: Artifact

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Other, description: \_\_\_\_\_

Doc Code: Artifact

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Other, description: \_\_\_\_\_

Doc Code: Artifact